## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Group Art Unit:
Examiner:
Applicant: James D. Coburn et al.
Serial No.:
Filing Date: Concurrently Herewith
For: METHOD OF PART FLOW MODEL FOR PROGRAMMABLE LOGIC CONTROLLER LOGICAL VERIFICATION SYSTEM

Assistant Commissioner for Patents Washington, D.C. 20231

Sir:

PTO Form-1449 is submitted herewith pursuant to the provisions of 37 CFR 1.97 and 1.98(a) as a means of complying with the requirements of 37 CFR 1.56 with respect to the above identified application. In accordance with Patent Office guidelines, copies of the citations listed on PTO-1449 are enclosed.

Respectfully submitted,

INFORMATION DISCLOSURE STATEM

By: Daniel H. Bliss

Reg. No. 32,398

BLISS McGLYNN & NOLAN, P.C. 2075 West Big Beaver Road, Suite 600 Troy, Michigan 48084 (248) 649-6090

Date:

Attorney Docket: 0693.00242 Ford Disclosure No.: 200-0664

## INFORMATION DISCLOSURE CITATION

(Use several sheets if necessary)

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SERIAL NO.

GROUP

Inventors: James D. Coburn and Joseph George Walacavage

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*EXAMINER	DOCUMENT NUMBER	DATE	NAME CLASS		SUBCLASS	FILING DAT	
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\*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

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Inventors: James D. Coburn and J seph Ge rge Walacavage

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